UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,842,056 B1

Page 1 of 1

APPLICATION NO.: 10/603722

DATED

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INVENTOR(S)

: Wong et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4,

Line 56, "...pase-locked loop" should read -- ...phase-locked loop --; Line 63, "...Claim 1, wherein" should read -- ...Claim 1, wherein: --; Line 67, "...signal, are..." should read -- ...signal are... --;

Column 6,

Lines 2 and 3, "...Phase-locked" should read -- ...phase-locked --; and Line 14, "locked loon..." should read -- locked loop... --.

Signed and Sealed this

Twenty-seventh Day of June, 2006

JON W. DUDAS

Director of the United States Patent and Trademark Office